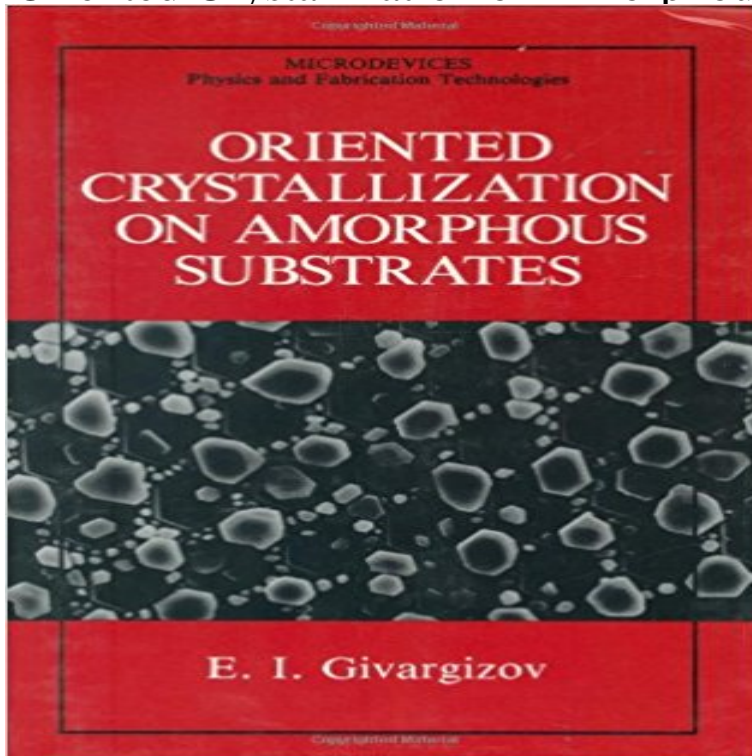


Oriented Crystallization on Amorphous Substrates (Microdevices)



Present-day science and technology have become increasingly based on studies and applications of thin films. This is especially true of solid-state physics, semiconductor electronics, integrated optics, computer science, and the like. In these fields, it is necessary to use films with an ordered structure, especially single-crystalline films, because physical phenomena and effects in such films are most reproducible. Also, active parts of semiconductor and other devices and circuits are created, as a rule, in single-crystal bodies. To date, single-crystalline films have been mainly epitaxial (or heteroepitaxial); i.e., they have been grown on a single-crystalline substrate, and principal trends, e.g., in the evolution of integrated circuits (ICs), have been based on continuing reduction in feature size and increase in the number of components per chip. However, as the size decreases into the submicrometer range, technological and physical limitations in integrated electronics become more and more severe. It is generally believed that a feature size of about $0.1\mu\text{m}$ will have a crucial character. In other words, the present two-dimensional ICs are anticipated to reach their limit of minimization in the near future, and it is realized that further increase of packing density and/or functions might depend on three-dimensional integration. To solve the problem, techniques for preparation of single-crystalline films on arbitrary (including amorphous) substrates are essential.

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